

CASE NO.: AM9-98-080C
Serial No.: 09/551,745
June 30, 2003
Page 2

PATENT
Filed: April 18, 2000

logic means for ordering the data access requests at the storage nodes based on the respective priorities, such that the data access requests are satisfied in consideration of their respective priorities.

Cancel Claim 7 and amend Claim 8 as follows:

B2 8. (amended) In a computer system having plural processors communicating data access requests to a shared storage system, a computer-implemented method for satisfying at least two contemporaneous data access requests to a single data storage device of the shared storage system, comprising the steps of:

responding to the requests in an order defined at least in part by one or more considerations external to the data storage device, wherein the one or more considerations external to the data storage device include a data request priority including a time-based deadline.

Cancel Claims 9 and 10 and amend Claims 11 and 13-15 to depend from Claim 8 as follows:

B3 11. (amended) The method of Claim 8, further comprising:
associating one or more of the data access requests with respective priorities;
sending the data access requests and priorities to storage nodes in the shared storage system, each storage node including at least one data storage device; and
ordering the data access requests at the storage nodes based on the respective priorities, such that the data access requests are satisfied in accordance with their respective priorities.

13. (amended) The method of Claim 8, further comprising:

1053-32C.AM1

CASE NO.: AM9-98-080C
Serial No.: 09/551,745
June 30, 2003
Page 3

PATENT
Filed: April 18, 2000

133
terminating at least one data access request, prior to the request being satisfied by a storage node.

14. (amended) The method of Claim 8, further comprising loosely synchronizing the computing and storage nodes with each other.

15. (amended) The method of Claim 8, wherein each storage node includes at least one storage computer and at least one data storage device, and the storage computer sends no more than one data access request at a time to the data storage device, such that the data storage device cannot reorder the sequence of responding to data access requests based on considerations internal to the data storage device.

Please amend Claim 17 as follows:

17. (amended) A computer program device comprising:
a computer program storage device readable by a digital processing apparatus; and
a program means on the program storage device and including instructions executable by the digital processing apparatus for performing method steps for satisfying one or more data access requests, the method steps comprising:

associating at least some of the data access requests with respective priorities, at least some of which are time-based; and

134
sending the priorities and the data access requests to a shared storage system, such that the shared storage system can respond to the requests in consideration of the priorities.

1053-32C.AM1

CASE NO.: AM9-98-080C
Serial No.: 09/551,745
June 30, 2003
Page 4

PATENT
Filed: April 18, 2000

Please amend Claim 22 as follows:

B5

22. (amended) A computer program device comprising:
a computer program storage device readable by a digital processing apparatus; and
a program means on the program storage device and including instructions executable by the digital processing apparatus for performing method steps for satisfying one or more data access requests, the method steps comprising:
responding, with a memory system, to at least some of the data access requests based on respective priorities, the priorities and the data access requests being sent to the memory system, wherein at least some priorities are time-based.

1053-32C.AM1